

Project 1: Using SystemVerilog via Quasta Sim (100 Points)

In this Project, your task is

1. Use the following tutorials to become familiar with Quasta Sim
 - a. [Getting started on Model Sim using Quartus Prime](#)
 - b. [Getting started using Quasta Sim](#)
2. Using Quasta Sim, design a full adder and simulate it using manual simulation by **forcing** individual inputs of various combinations.
3. Add a set of test cases to your design case in order to fully test the full adder. Use **run all** option to display the waveform.
4. Create a separate design and testbench files and repeat the simulation.

Your report should include your code and simulation waveform for steps 2 through 4. .